

	L #	Hits	Search Text	DBs	Time Stamp
1	L1	618	delay and clock adj generat\$3 and (single adj end or differential) adj2 (clock or signal)	USPAT; EPO; JPO; DERWENT	2002/12/24 12:49
2	L2	251	1 and ("PLL" or phase adj2 loop)	USPAT; EPO; JPO; DERWENT	2002/12/24 11:45
3	L3	367	1 not 2	USPAT; EPO; JPO; DERWENT	2002/12/24 11:45
4	L4	65	3 and 327/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/12/24 11:47
5	L5	148	generat\$3 with single\$1 adj end\$2 adj2 signal	USPAT; EPO; JPO; DERWENT	2002/12/24 11:49
6	L6	46	5 and delay	USPAT; EPO; JPO; DERWENT	2002/12/24 11:49
7	L7	63	delay and clock adj generat\$3 and (single adj ended) adj2 (clock or signal)	USPAT; EPO; JPO; DERWENT	2002/12/24 13:35
8	L8	15861	delay and 327/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/12/24 13:36
9	L9	373	8 and MUX	USPAT; EPO; JPO; DERWENT	2002/12/24 13:37
10	L10	146	9 and ("PLL" or phase adj2 loop)	USPAT; EPO; JPO; DERWENT	2002/12/24 13:48
11	L11	126	9 and ("XOR" or exclusive adj"OR")	USPAT; EPO; JPO; DERWENT	2002/12/24 14:02
12	L12	67	11 not 10	USPAT; EPO; JPO; DERWENT	2002/12/24 13:48
13	L13	538	8 and ("XOR" or exclusive adj"OR") and ("PLL" or phase adj2 loop)	USPAT; EPO; JPO; DERWENT	2002/12/24 14:02
14	L14	479	13 not 10	USPAT; EPO; JPO; DERWENT	2002/12/24 14:03
15	L15	479	14 not 11	USPAT; EPO; JPO; DERWENT	2002/12/24 14:03